

Notice of References Cited	Application/Control No. 09/410,160		Applicant(s)/Patent Under Reexamination BELL ET AL.	
	Examiner Dwin M Craig		Art Unit 2123	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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*	B	US-5,539,652 A	07-1996	Tegethoff, Mauro V.	703/14
*	C	US-6,026,228 A	02-2000	Imai et al.	716/18
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Mark Arnold, Anthony Wallace, Jerry Cupal, John Cowles, "Towards a Formal Model of Hardware Synthesized from Verilog" IEEE 1996, pages 60-66.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.